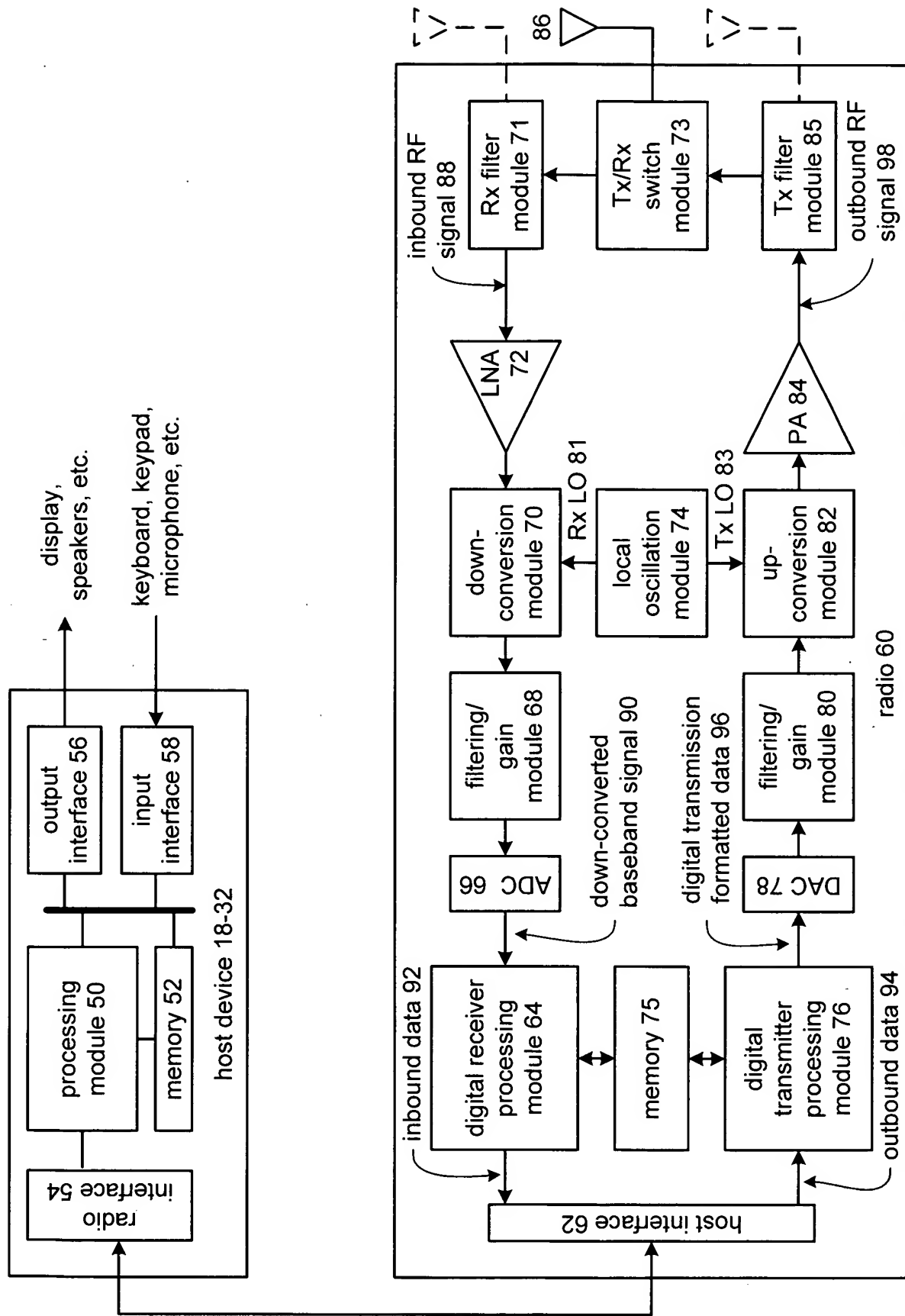
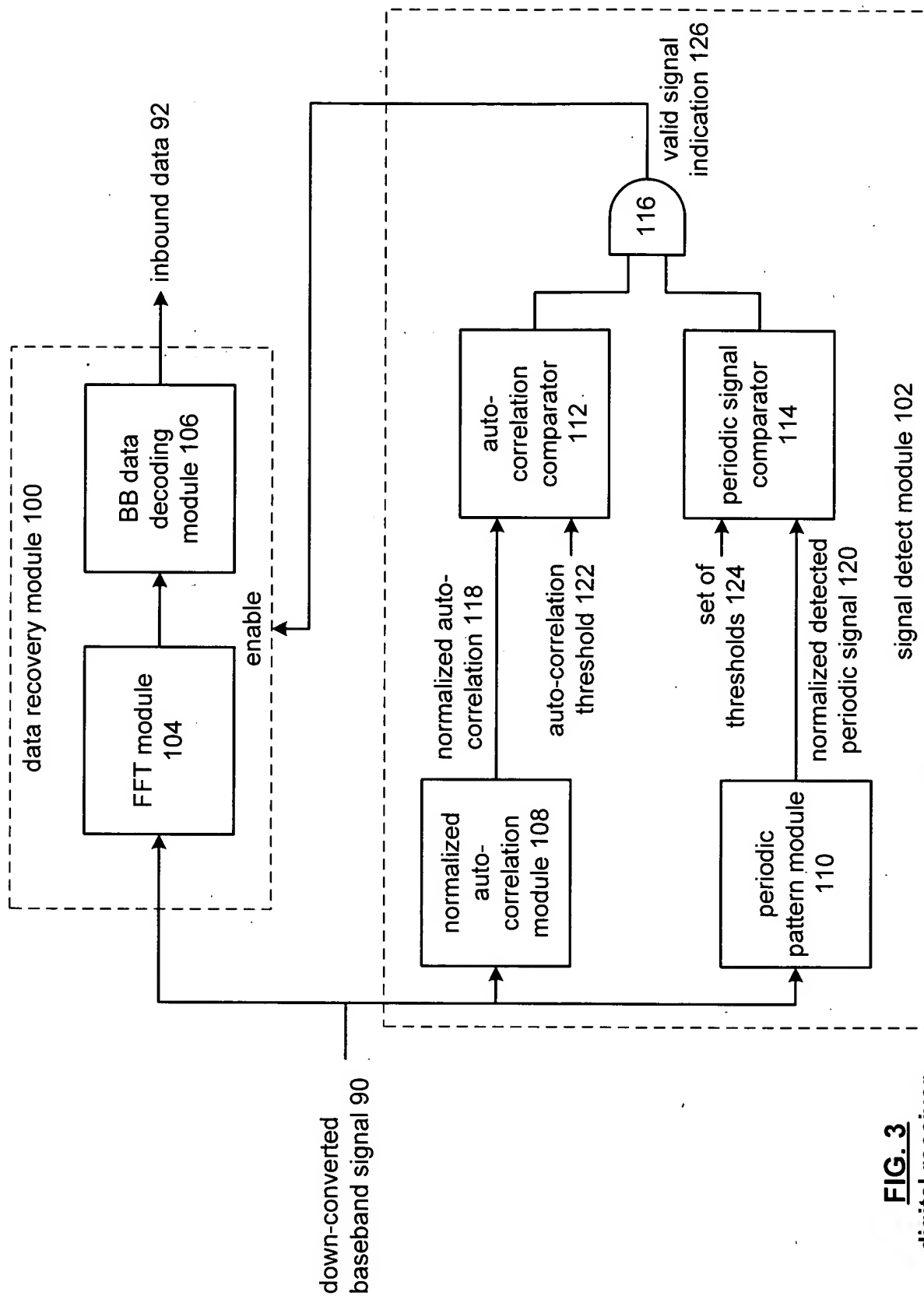


10

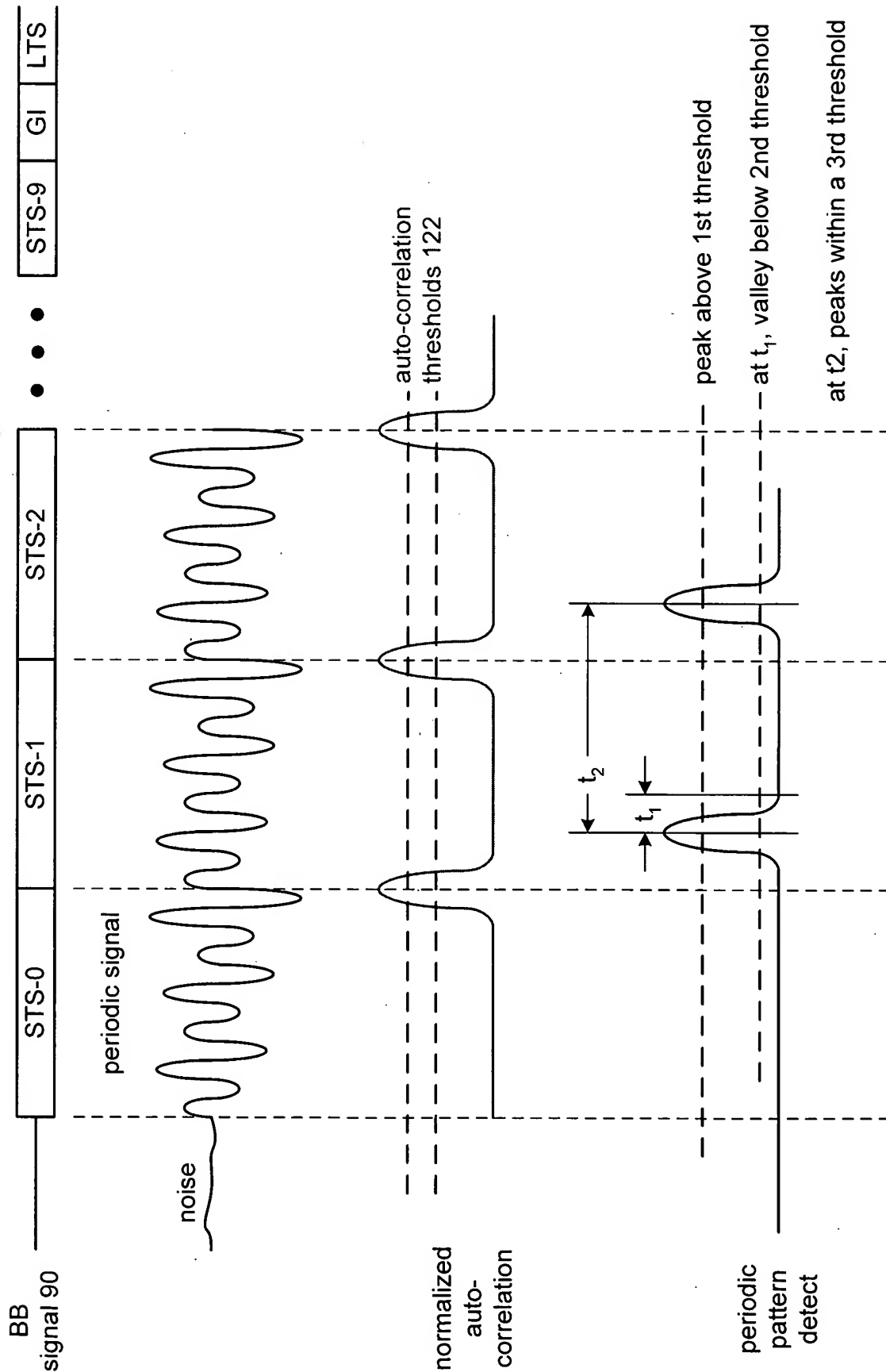
**FIG. 1**



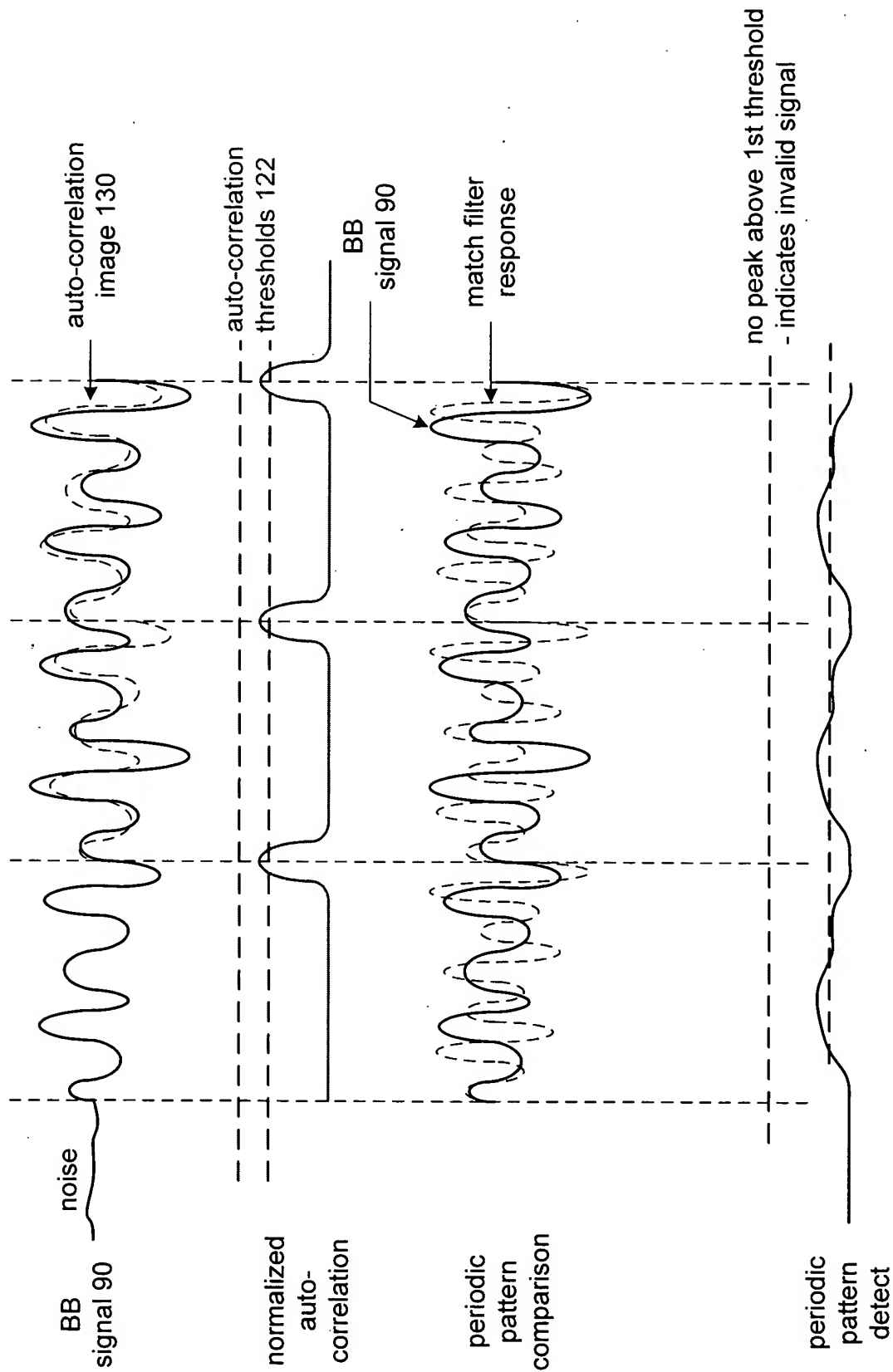
**FIG. 2**



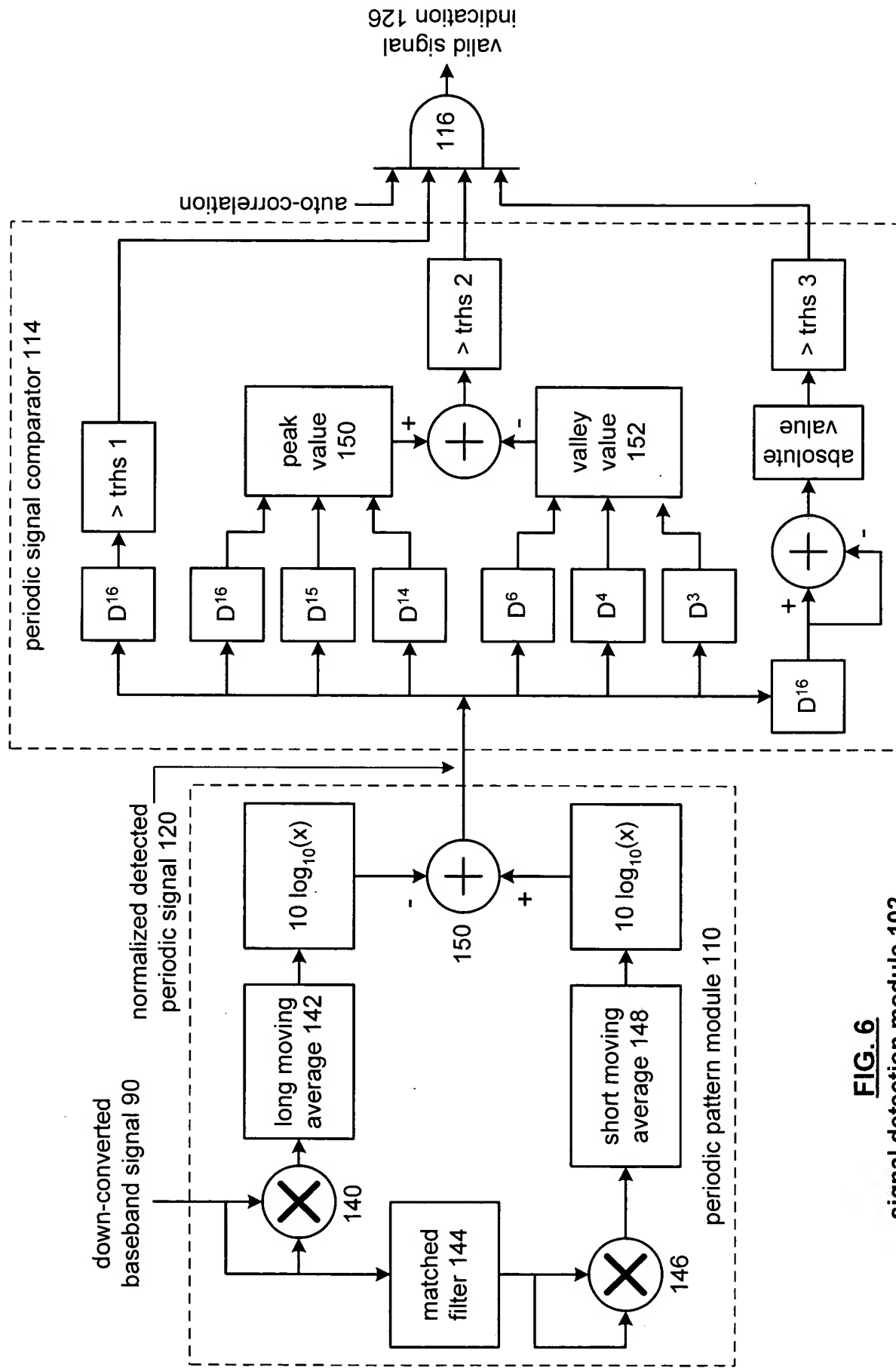
**FIG. 3**  
digital receiver  
processing module 64



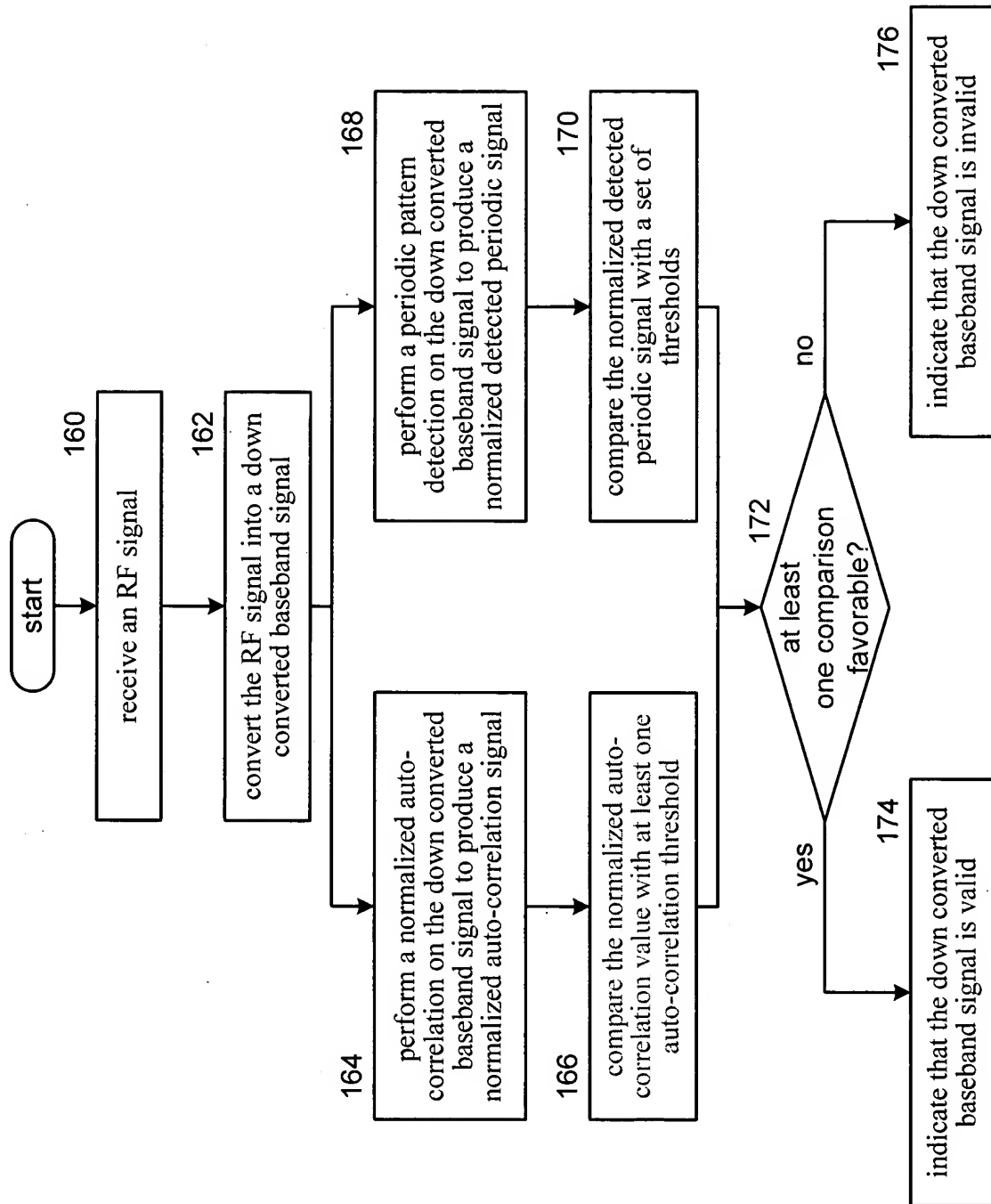
**FIG. 4**  
valid signal detection



**FIG. 5**  
invalid signal detection



**FIG. 6**  
signal detection module 102



**FIG. 7**